

ME VLSI (II Year)

Semester III

COURSE NAME	Testing and Verification of VLSI Circuits
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At the end of this course, the student will be able to:

COURSE NAME	COURSE OUTCOMES
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CO1	Accept challenges in VLSI Testing at different abstraction levels
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CO2	Understand fault models for generation of test vectors
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	Calculate observability and controllability parameters of circuit
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	Enhance testability of a circuit
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	Use simulation techniques for designing and testing of VLSI circuits
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CO3	Identify characteristics of verification methods
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COURSE NAME	ASIC Design
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At the end of this course, the student will be able to:

COURSE NAME	COURSE OUTCOMES
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CO1	Understand concepts and techniques of ASIC modeling and synthesis
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CO2	Perform static timing analysis, delay estimation and synchronization
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CO3	Learn ASIC Construction and testing techniques
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CO1	The student will understand concept of static and dynamic reconfiguration
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CO2	The student will use the basics of the PLDs for designing reconfigurable circuits
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CO3	The student will understand the reconfigurable system design using HDL
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