

**ME VLSI (I Year)****Semester I**

<b>COURSE NAME</b>	<b>Embedded System Design</b>
At the end of this course, the student will be able to:	
<b>Course Outcome</b>	<b>Course Outcome</b>
CO1	The student will study ARM Processor based Embedded System design
CO2	The student will be able to do programming in Embedded programming in C,C++
CO3	The student will understand Linux operating system and device driver
CO4	The student will demonstrate the knowledge of android operating system

<b>COURSE NAME</b>	<b>Reconfigurable Computing</b>
At the end of this course, the student will be able to:	
<b>Course Outcome</b>	<b>Course Outcome</b>
CO1	The student will understand concept of static and dynamic reconfiguration
CO2	The student will use the basics of the PLDs for designing reconfigurable circuits
CO3	The student will understand the reconfigurable system design using HDL

**Semester II**

<b>COURSE NAME</b>	<b>Analog CMOS Design</b>
At the end of this course, the student will be able to:	
<b>COURSE NAME</b>	<b>COURSE OUTCOMES</b>
CO1	The student will understand the fundamentals of CMOS Technology in Analog Domain
CO2	The student will show the skills of designing CMOS analog circuits
CO3	The student will demonstrate the ability for using backend tools in analog IC technology

<b>COURSE NAME</b>	<b>System on Chip</b>
At the end of this course, the student will be able to:	
<b>COURSE NAME</b>	<b>COURSE OUTCOMES</b>
CO1	The student will learn to design flow graphs and flow modeling
CO2	The student will study SOC modeling and interfacing
CO3	The student will learn SOC memory system design, embedded software and energy management techniques for SOC design, SOC prototyping, verification, testing and physical design
CO4	The student will able to design , implement and test SOC